

CHARLES LO

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EDUCATION

Georgia Institute of Technology | Atlanta, GA

Aug 2026 ~ Present

M.S. Computer Science, Computing Systems Specialization

Relevant coursework: Graduate Operating Systems, Machine Learning

University of Wisconsin-Madison | Madison, WI

Sep 2022 ~ May 2026

B.S. Computer Science, Physics; Undergraduate Certificate in Entrepreneurship

Relevant coursework: Computer Architecture, Digital Systems, Operating Systems, Signals and Systems (DSP), Algorithms, UI/UX

ENGINEERING EXPERIENCE

Coherent Corp. | Fremont, CA

June 2026 ~ Present

RF Engineer Intern - CPO Software Test Automation Development

- Developed COBS, a Python/Tkinter application that coordinates two Keysight N7733C optical switches as a unified 32-channel test system.
- Integrated PyVISA/SCPI, serial, and vendor-driver interfaces for optical routing, live power monitoring, FlexDCA measurements, and eye-image capture.
- Automated 32-channel optical-power and DCA characterization workflows with configurable settling, retry handling, Excel reporting, and PNG exports.
- Implemented instrument identity checks, route readback verification, active-path interlocks, error isolation, and mock/dry-run modes for safer bench operation.
- Built mocked instrument adapters and a software regression suite covering routing, automation, GUI behavior, measurement workflows, and report generation.
- Packaged the application as a standalone Windows executable and documented its architecture, setup, validation procedures, and operational limitations.

Lintes Technology Co. Ltd | New Taipei City, Taiwan

May 2023 ~ Aug 2023

Firmware Engineer Intern

- Developed C and C# utilities to support low-cost bit error rate testing of high-speed DAC cables.
- Modeled transmission-line behavior and evaluated signal integrity performance using QucsStudio.
- Set up and configured production-line test equipment for cable validation workflows.
- Supported troubleshooting and verification of test setups during production bring-up.

Lintes Technology Co. Ltd | New Taipei City, Taiwan

May 2024 ~ Aug 2024

SI Engineer Intern

- Characterized high-speed interconnects using S-parameter measurements, including impedance, insertion loss, return loss, and mode conversion.
- Operated a vector network analyzer to evaluate signal integrity across interconnect channels.
- Developed Python-based control scripts for microcontroller-driven switching hardware used in test setups.
- Assisted with engineering prototype debugging through HFSS simulations and optical power measurements.

ÜJIN Media | Chicago, IL

May 2025 ~ June 2026

Co-Founder, Product & Engineering

- Architected and developed a full-stack web platform using React, FastAPI, and MongoDB to automate personalized portfolio website creation.
- Built REST APIs, authentication workflows, image-processing features, and real-time website preview functionality.
- Integrated AI-assisted content and layout-generation features into the platform.
- Led a four-person cross-functional team, translating product requirements into technical milestones and development plans.
- Helped advance the company into StartingBlock Madison's Level Set startup incubator program.

TECHNICAL SKILLS

Programming: Python, C, C#, Java, MATLAB, React, FastAPI **Hardware/Test:** PyVISA, SCPI, oscilloscopes/DCA, VNA, optical power meters, BER testing, microcontrollers

Tools: Git, QucsStudio, HFSS, MongoDB, Linux/Windows **Languages:** English and Mandarin Chinese (fluent), Spanish (intermediate)